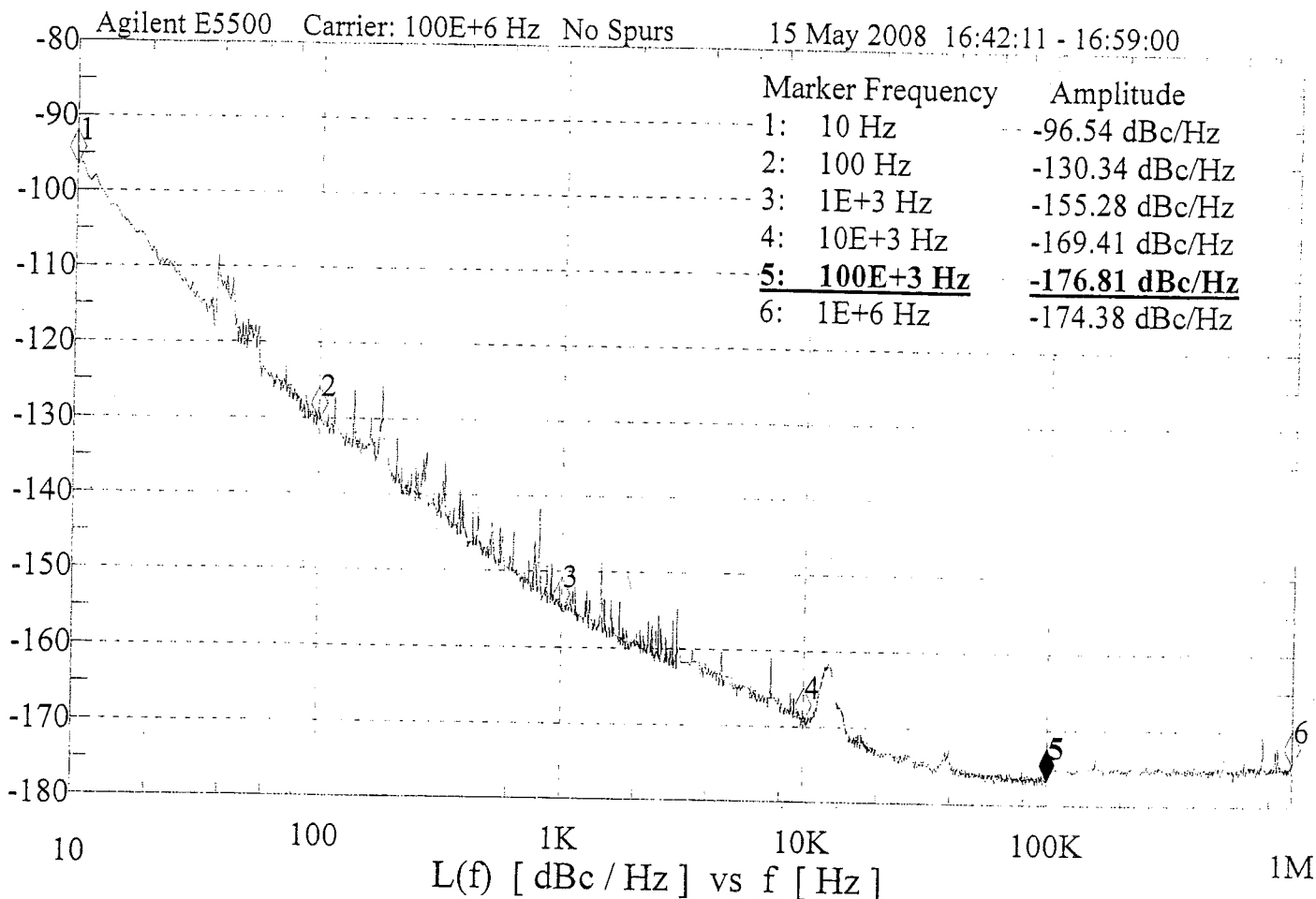


# Bliley 100MHz



## Bliley 100MHz

Measurement time: 15 May 2008 16:42:11 - 16:59:00

Measurement type: Absolute phase noise (using a phase locked loop)

Start offset frequency: 10 Hz

Stop offset frequency: 1E+6 Hz

Minimum number of FFT averages: 16

Carrier Source frequency: 100E+6 Hz

Detector input frequency: 100E+6 Hz

Detector: Test set RF phase detector

Nominal VCO tune constant: 80 Hz/Volt

VCO center voltage: 4.9 Volts

VCO tune range: 5 Volts

Detector constant cal method: Derive from measured beatnote.

Detector constant: 597.6E-3 V/Rad

VCO tune constant cal method: Use the current Tune Constant.

Current VCO tune constant: 74.73 Hz/Volt

PLL Integrator attenuation: 0 dB

Phase Locked Loop suppression was verified.

Closed PLL BW: 71.63 Hz

Peak Tune Range: 328.01 Hz

Assumed Pole: 2.117E+3 Hz

Carrier Source: (manual)

Reference Source: (manual) ; VCO tuned using EFC.

Time Base: (none)

Downconverter: (none)

LNA gain: 42 dB

Software Version: A.01.05



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